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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10047013	FILING DATE 01/16/2002	CLASS 257	SUBCLASS -	GAU 2814	EXAMINER Erden
**APPLICANTS: Rosner Wolfgang; Schulz Thomas; Risch Lothar; Augle Thomas; Schafer Herbert; Franosch Martin;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF PCT/DE00/02316 07/17/2000					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 199 33 564.8 07/16/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO L&L-10232	
TITLE : Method for producing a vertical semiconductor transistor component and vertical semiconductor transistor component					
U S DEPT OF COMMERCE PAT & TM-PTO-435L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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